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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*  
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IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 5	TOTAL CLAIMS 18	INDEPENDENT CLAIMS 3
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Verified and Acknowledged

Examiner's Signature \_\_\_\_\_ Initials \_\_\_\_\_

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TITLE  
Semiconductor integrated circuit including anti-fuse and method for manufacturing the same

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